

FIG. 1

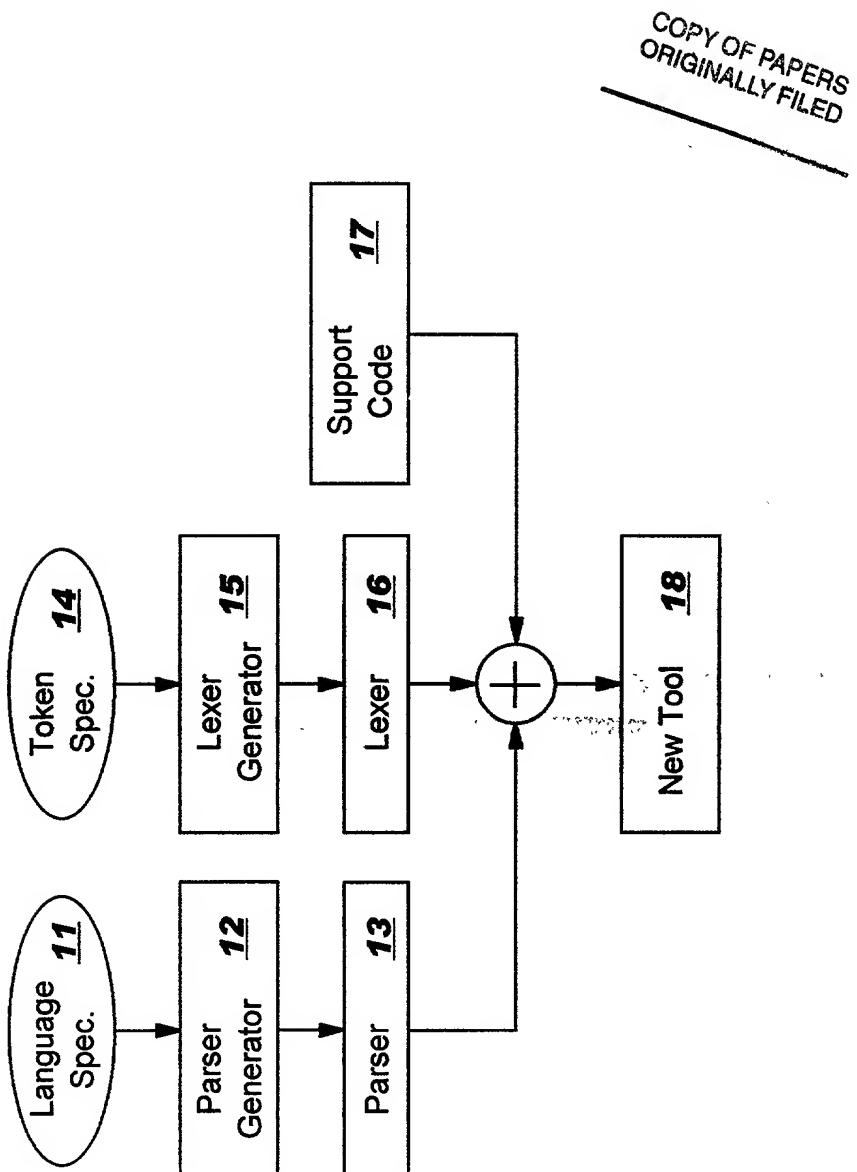


FIG. 2
(Prior Art)

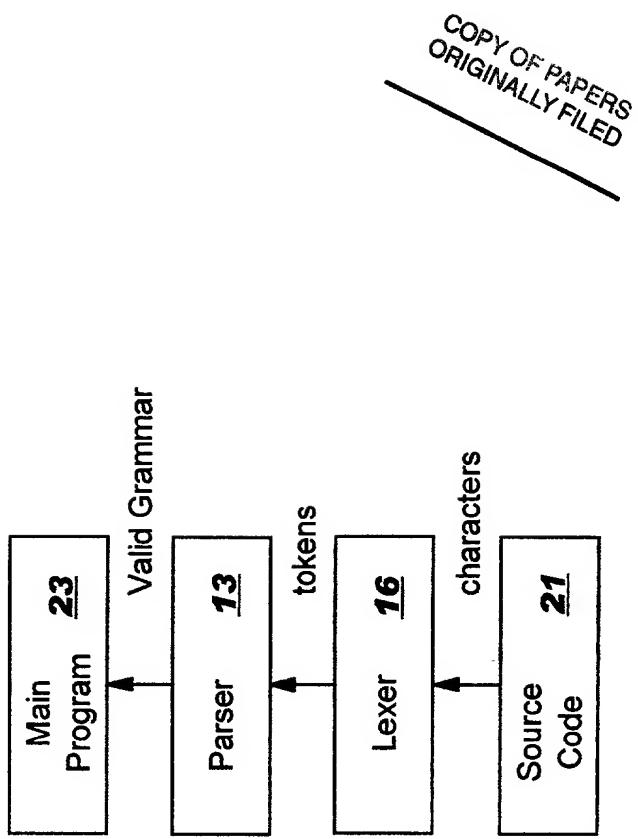
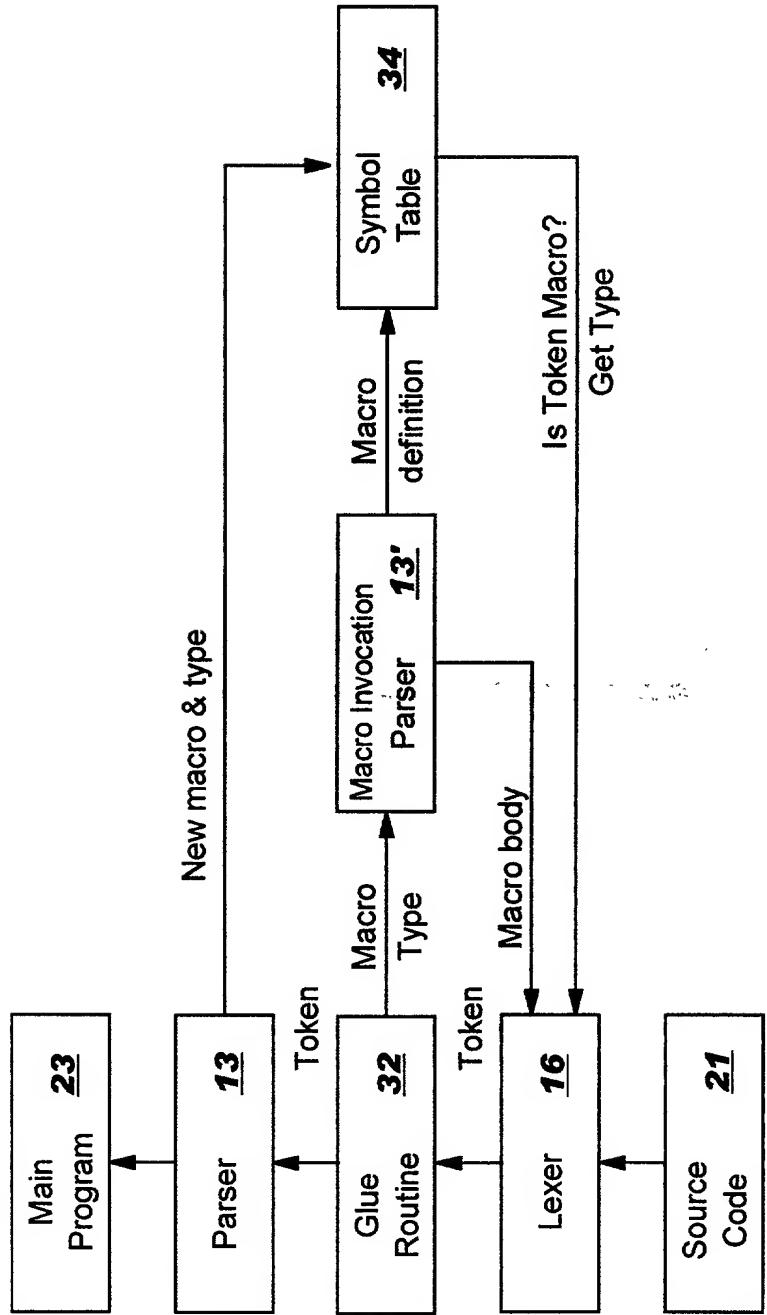


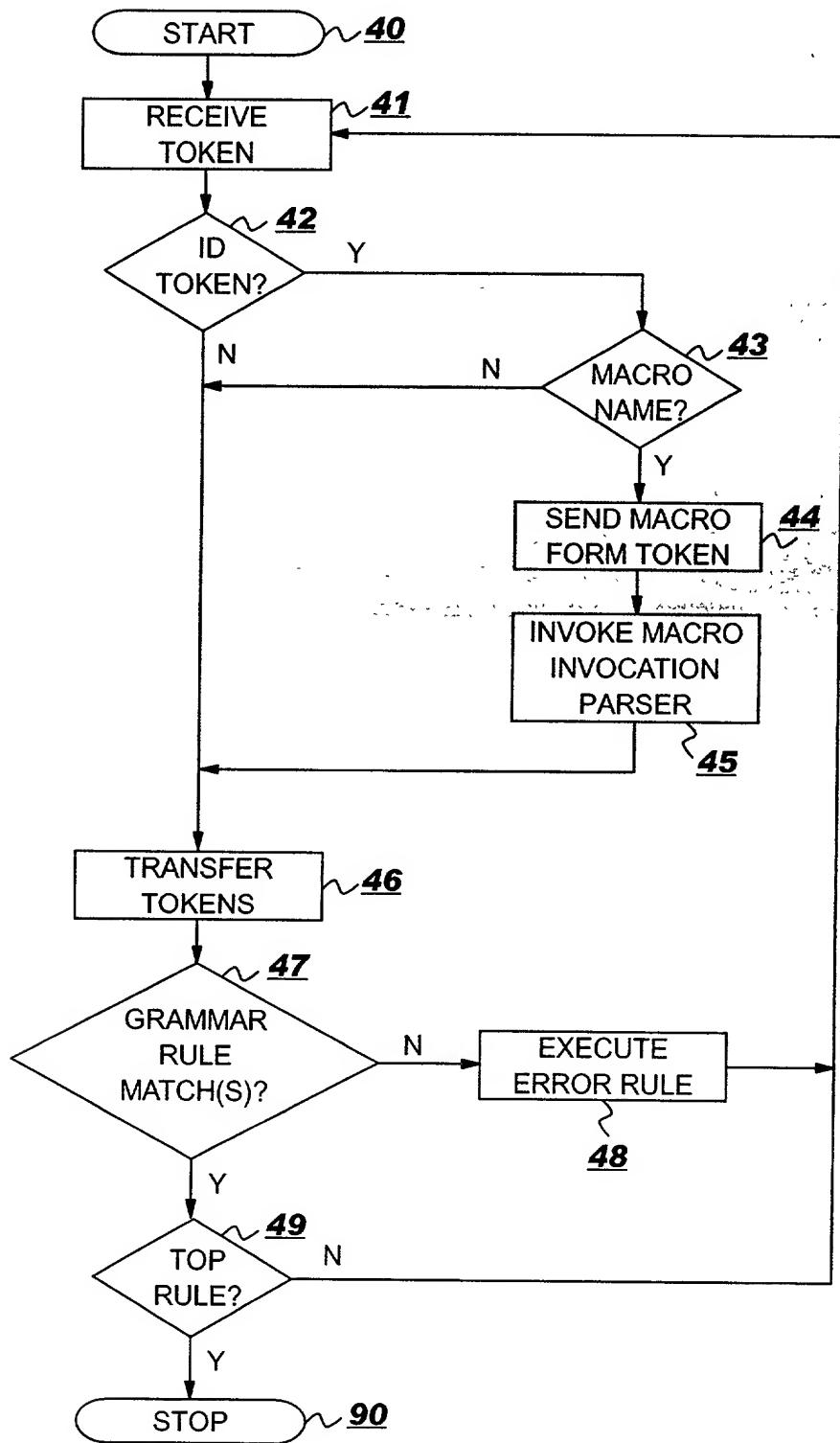
FIG. 3



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FIG. 4



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FIG. 5

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Embedded Pre-Processor with Dynamic Macro Forms
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